WHAT IS CLAIMED IS:

1. A method for providing synchronization for an additional downlink channel comprising:

at a coprocessor,

generating a synchronization strobe as a response to a triggering event;

determining a time difference based upon the time stamp;

at a modem,

saving a time stamp when the synchronization strobe is received; and

providing the time stamp to the coprocessor.

2. The method of claim 1, wherein the time stamp is a counter's value when the synchronization strobe is received.

3. The method of claim 2, wherein the counter is reset after the time stamp is saved.

4. The method of claim 1, wherein the time difference is used to set a code offset for a received signal demodulator at the coprocessor.

5. The method of claim 1, wherein the time difference is the difference between the time stamp and a second time stamp.

6. The method of claim 5, wherein the second time stamp is saved when the synchronization strobe is generated.

7. The method of claim 5, wherein the second time stamp is saved at the coprocessor.

8. A method for providing an additional channel comprising:

detecting a timing for a transmission;

selecting a time to insert an extra transmission for the additional channel into the transmission; and

inserting the extra transmission for the additional channel into the transmission.

9. The method of claim 8, wherein the timing is a frame timing, and wherein the extra transmission is a frame.

10. The method of claim 8, wherein the timing is a slot timing, and wherein the extra transmission is a slot.

11. The method of claim 8 further comprising prior to the detecting, selecting a branch of the transmission.

12. The method of claim 11, wherein the transmission comprises two branches, and wherein the selecting comprises selecting the branch with greater transmit power.

13. The method of claim 12, wherein a comparison of baseband transmit power gain factors is used to determine the branch with greater transmit power.

14. The method of claim 8, wherein the detecting comprises determining the timing of a modem uplink transmission with information available about the modem's transmission.

15. The method of claim 14, wherein the information comprises a channelization code, a scrambling code, or a combination thereof.

16. The method of claim 8 further comprising prior to the detecting:

determining if there is a need to transmit on the additional channel; and

performing the detecting, selecting, and inserting only if there is a need to transmit on the additional channel.

17. The method of claim 8 further comprising after the inserting:

tracking the timing changes; and

repeating the selecting and inserting.

18. The method of claim 17, wherein the tracking comprises:

determining the timing for a previous transmission;

determining the timing for a current transmission; and

providing a timing adjustment based on a difference between the timings.

19. A circuit for use in providing an additional channel comprising:

a sync and tracking unit coupled to a transmission input, the sync and tracking unit containing circuitry to synchronize the circuit to a timing of a transmission provided by the transmission input;

a matched filter coupled to the sync and tracking unit, the matched filter containing circuitry to determine the timing of the transmission;

a subframe generator containing circuitry to create a data unit for transmission on the additional channel; and

a data generation unit coupled to the transmission input, the sync and tracking unit, and the subframe generator, the data generation unit containing circuitry to encode and modulate the data unit and to insert the data unit into the transmission.

20. The method of claim 19, wherein the timing is a frame timing, and wherein the data unit is a frame.

21. The method of claim 19, wherein the timing is a slot timing, and wherein the data unit is a slot.

22. The circuit of claim 19, wherein the sync and tracking unit comprises:

a sequence register coupled to the matched filter, the sequence register containing a sequence that is based on channelization code of a stronger of two branches in the transmission;

a pulse shaping filter coupled to the transmission input and the matched filter, the pulse shaping filter to decode the transmission; and

a timing tracking unit coupled to the data generating unit, the time tracking unit

containing circuitry to adjust sample timing of the data generating unit to keep it in sync with the transmission's timing.

23. The circuit of claim 22, wherein the sequence stored in the sequence register is the conjugate of a point wise product of the scrambling code and a channelization code of the stronger of the two branches.

24. The circuit of claim 22, wherein the sync and tracking unit can keep track of a shifting transmit timing in the transmission.

25. The circuit of claim 19, wherein the matched filter descrambles the transmission with a sequence based on a channelization code of a stronger of two branches in the transmission.

26. The circuit of claim 19, wherein the data generation unit adds the encoded and modulated data unit with the transmission.

27. The circuit of claim 26, wherein the data generation unit scrambles, gain modifies, and spreads the data unit with a channelization code and a scrambling code provided by a scrambling code generator.

28. The circuit of claim 19, wherein when there is no data unit to transmit, no data units are inserted into the transmission.

29. A wireless device comprising:

a modem coupled to a radio frequency (RF) circuit, the modem containing circuitry to encode and modulate a data stream to provide to the RF circuit for data transmission purposes and demodulate and decode a received signal from the RF circuit for data reception purposes, wherein the modem implements a first version of a technical specification; and

a coprocessor coupled to the modem and the RF circuit, the coprocessor containing circuitry to encode and modulate a data stream to provide to the RF circuit for data transmission purposes and demodulate and decode a received signal from the RF circuit for data reception purposes, wherein the coprocessor implements a second version of the technical specification.

30. The wireless device of claim 29, wherein the second version of technical specification is a superset of the first version of the technical specification.

31. The wireless device of claim 30, wherein the coprocessor implements a portion of the second version of the technical specification not included in the first version of the technical specification.

32. The wireless device of claim 29, wherein the coprocessor comprises:

a sync and tracking unit coupled to a modem, the sync and tracking unit containing circuitry to synchronize the circuit to a timing of a transmission provided by the modem;

a matched filter coupled to the sync and tracking unit, the matched filter containing circuitry to determine the timing of the transmission;

a subframe generator containing circuitry to create a data unit for transmission on the additional channel; and

a data generation unit coupled to the transmission input, the sync and tracking unit, and

the subframe generator, the data generation unit containing circuitry to encode and modulate the data unit and to insert the data unit into the transmission.

33. The wireless device of claim 29, wherein the wireless device is used in a wireless communications system.

34. The wireless device of claim 33, wherein the wireless communications system is a UMTS Release 5 compliant system.

35. The wireless device of claim 33, wherein the wireless communications system is a CDMA Release C compliant system.